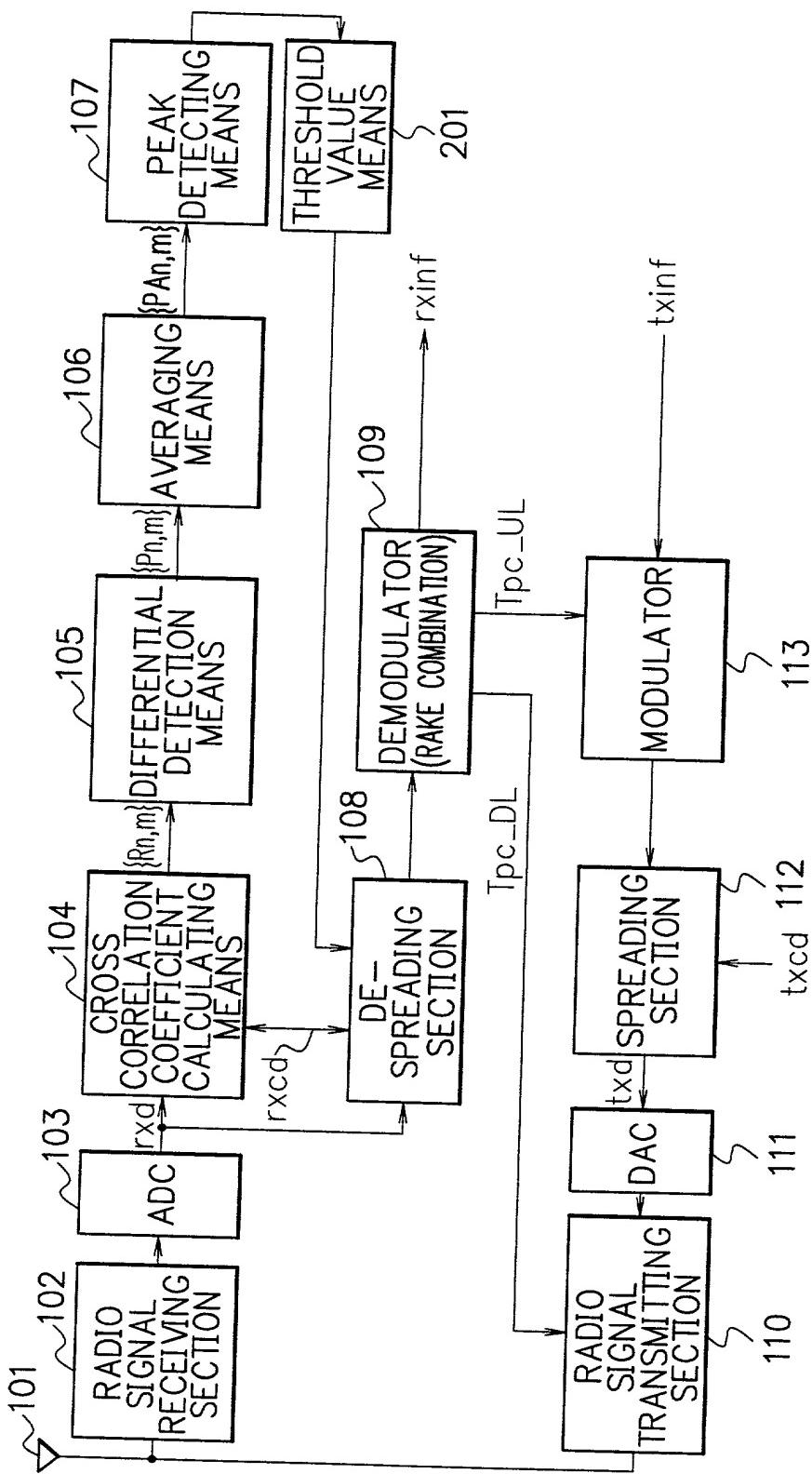


F I G. 1



Toshifumi SATO

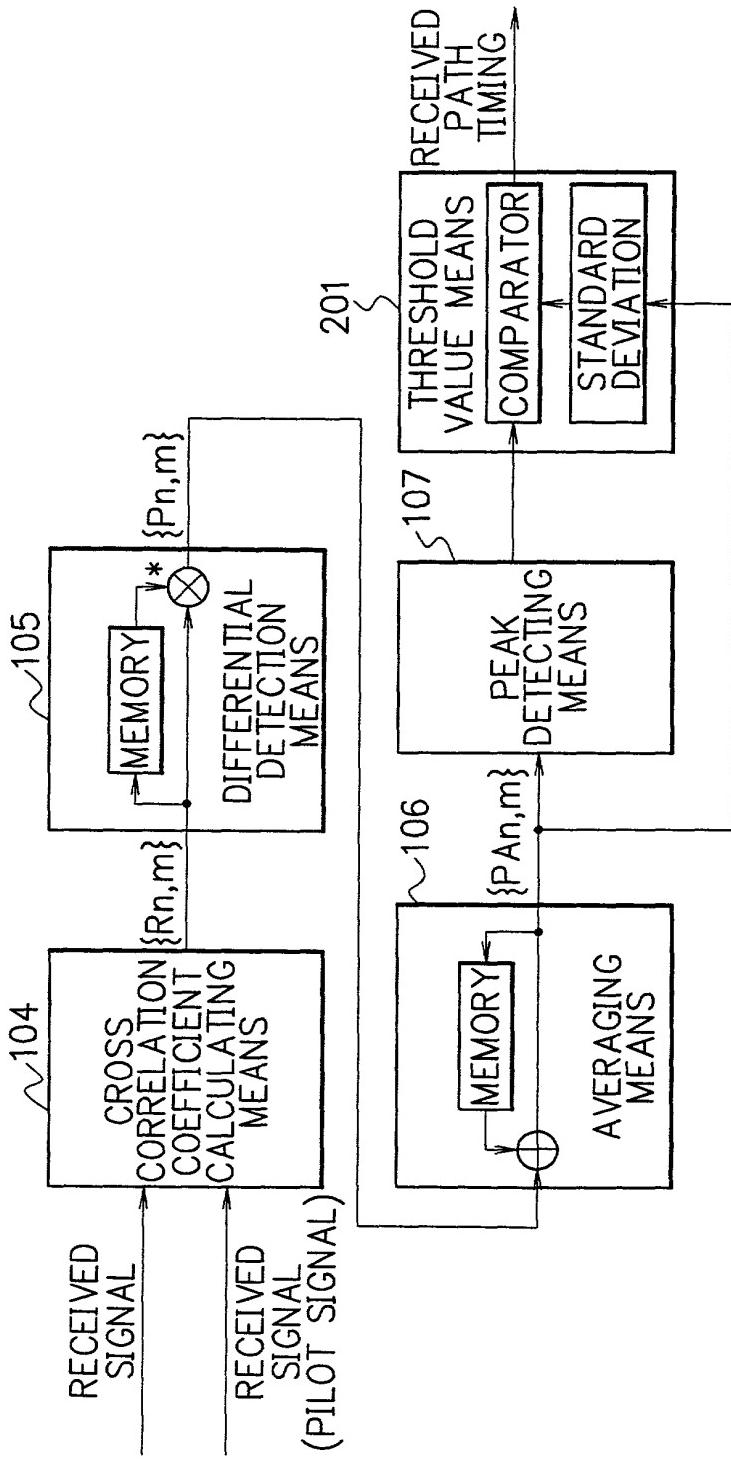
"Received Path Timing Detecting Circuit at DS-CDMA System"

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Filed June 7, 2001

Sheets 2 of 3

F I G. 2



F I G. 3

